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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *None-LV*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *YES LV*  
 JAPAN P2000-131626 04/28/2000

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>LV</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 2
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ADDRESS  
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TITLE  
 Method for manufacturing semiconductor device, and processing system and semiconductor device

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